

## NPN MEDIUM POWER TRANSISTOR

#### **Features**

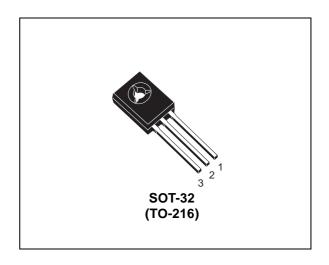
- **■** HIGH CURRENT
- LOW SATURATION VOLTAGE
- COMPLEMENT TO 2SB772

## **Applications**

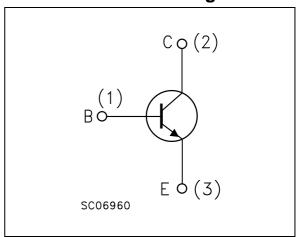
- VOLTAGE REGULATION
- RELAY DRIVER
- GENERIC SWITCH
- AUDIO POWER AMPLIFIER
- DC-DC CONVERTER



The device is a NPN transistor manufactured by using planar Technology resulting in rugged high performance devices. The complementary PNP type is 2SB772.



## **Internal Schematic Diagram**



#### **Order Codes**

| Part Number | Marking | Package | Packing |
|-------------|---------|---------|---------|
| 2SD882      | D882    | SOT-32  | TUBE    |

# 1 Absolute Maximum Ratings

Table 1. Absolute Maximum Rating

| Symbol           | Parameter                                      | Value      | Unit |
|------------------|--|------------|------|
| V <sub>CBO</sub> | Collector-Base Voltage (I <sub>E</sub> = 0)    | 60         | V    |
| V <sub>CEO</sub> | Collector-Emitter Voltage (I <sub>B</sub> = 0) | 30         | V    |
| $V_{EBO}$        | Collector-Base Voltage (I <sub>C</sub> = 0)    | 5          | V    |
| I <sub>C</sub>   | Collector Current                              | 3          | Α    |
| I <sub>CM</sub>  | Collector Peak Current (t <sub>P</sub> < 5ms)  | 6          | Α    |
| Ι <sub>Β</sub>   | Base Current                                   | 1          | Α    |
| $I_{BM}$         | Base Peak Current (t <sub>P</sub> < 5ms)       | 2          | Α    |
| $P_{TOT}$        | Total dissipation at T <sub>c</sub> = 25°C     | 12.5       | W    |
| $T_{STG}$        | Storage Temperature                            | -65 to 150 | °C   |
| $T_J$            | Max. Operating Junction Temperature            | 150        | °C   |

Table 2. Thermal Data

| Symbol                | Parameter                            | Value | Unit |
|-----------------------|--------------------------------------|-------|------|
| R <sub>thJ-case</sub> | Thermal Resistance Junction-Case Max | 10    | °C/W |

2SD882 2 Electrical Characteristics

# 2 Electrical Characteristics

Table 3.Electrical Characteristics ( $T_{CASE} = 25^{\circ}C$ ; unless otherwise specified)

| Symbol                       | Parameter  | Test Conditions   | Min. | Тур. | Max.              | Unit        |
|------------------------------|--|---|------|------|-------------------|-------------|
| I <sub>CES</sub>             | Collector Cut-off Current (V <sub>BE</sub> = 0)                | V <sub>CE</sub> = 60 V  |      |      | 10                | μA          |
| I <sub>CEO</sub>             | Collector Cut-off Current (I <sub>B</sub> = 0)                 | V <sub>CE</sub> = 30 V  |      |      | 100               | μA          |
| I <sub>EBO</sub>             | Emitter Cut-off Current (I <sub>C</sub> = 0)                   | V <sub>EB</sub> = 5 V   |      |      | 10                | μA          |
| V <sub>(BR)CEO</sub> Note: 1 | Collector-Emitter Breakdown<br>Voltage<br>(I <sub>B</sub> = 0) | I <sub>C</sub> = 10 mA  | 30   |      |                   | V           |
| V <sub>(BR)CBO</sub>         | Collector-Base Breakdown Voltage ( $I_E = 0$ )                 | I <sub>C</sub> = 100 μA   | 60   |      |                   | V           |
| V <sub>(BR)EBO</sub>         | Emitter-Base Breakdown Voltage ( $I_C = 0$ )                   | I <sub>E</sub> = 100 μA   | 5    |      |                   | V           |
| V <sub>CE(sat)</sub> Note: 1 | Collector-Emitter Saturation<br>Voltage                        | $I_C = 1 \text{ A}$ $I_B = 50 \text{ mA}$<br>$I_C = 2 \text{ A}$ $I_B = 100 \text{ mA}$<br>$I_C = 3 \text{ A}$ $I_B = 150 \text{ mA}$     |      |      | 0.4<br>0.7<br>1.1 | V<br>V<br>V |
| V <sub>BE(sat)</sub> Note: 1 | Base-Emitter Saturation Voltage                                | I <sub>C</sub> = 2 A I <sub>B</sub> = 100 mA  |      |      | 1.2               | V           |
| hFE                          | DC Current Gain  | $I_{C} = 100 \text{ mA}$ $V_{CE} = 2 \text{ V}$ $I_{C} = 1 \text{ A}$ $V_{CE} = 2 \text{ V}$ $I_{C} = 3 \text{ A}$ $V_{CE} = 2 \text{ V}$ | 80   |      | 300               |             |
| fT                           | Transition Frequency   | $I_C = 0.1 \text{ A}$ $V_{CE} = 10 \text{ V}$   |      | 100  |                   | MHz         |

Note: 1 Pulsed duration = 300  $\mu$ s, duty cycle  $\leq$ 1.5%.

2 Electrical Characteristics 2SD882

# 2.1 Typical characteristics

Figure 1. Reverse biased area

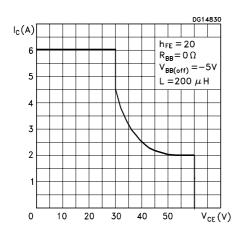


Figure 2. DC current gain

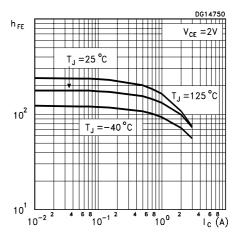
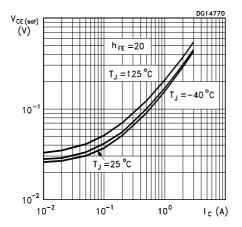
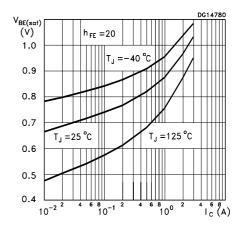


Figure 3. Collector-emitter saturation voltage Figure 4. Base-emitter saturation voltage



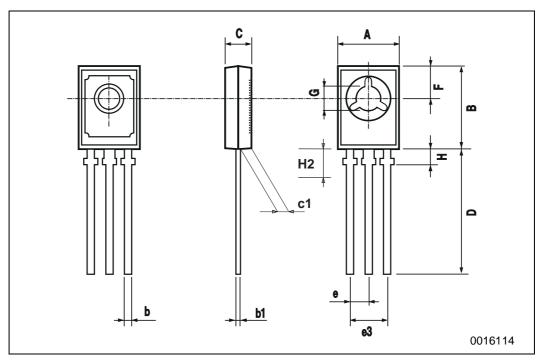


# 3 Package Mechanical Data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: <a href="https://www.st.com">www.st.com</a>

### SOT-32 (TO-126) MECHANICAL DATA

| DIM.  | mm   |      |      | inch  |       |       |
|-------|------|------|------|-------|-------|-------|
| DIWI. | MIN. | TYP. | MAX. | MIN.  | TYP.  | MAX.  |
| А     | 7.4  |      | 7.8  | 0.291 |       | 0.307 |
| В     | 10.5 |      | 10.8 | 0.413 |       | 0.445 |
| b     | 0.7  |      | 0.9  | 0.028 |       | 0.035 |
| b1    | 0.49 |      | 0.75 | 0.019 |       | 0.030 |
| С     | 2.4  |      | 2.7  | 0.040 |       | 0.106 |
| c1    | 1.0  |      | 1.3  | 0.039 |       | 0.050 |
| D     | 15.4 |      | 16.0 | 0.606 |       | 0.629 |
| е     |      | 2.2  |      |       | 0.087 |       |
| e3    | 4.15 |      | 4.65 | 0.163 |       | 0.183 |
| F     |      | 3.8  |      |       | 0.150 |       |
| G     | 3    |      | 3.2  | 0.118 |       | 0.126 |
| Н     |      |      | 2.54 |       |       | 0.100 |
| H2    |      | 2.15 |      |       | 0.084 |       |



4 Revision History 2SD882

# 4 Revision History

| Date        | Revision | Changes                       |
|-------------|----------|-------------------------------|
| 09-Sep-2005 | 2        | Final datasheet. New template |

2SD882 4 Revision History

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners

© 2005 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com

